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## Rice Grading System for Embedded Image Processing

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By Gurpreet Kaur

LAP Lambert Academic Publishing Okt 2014, 2014.

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Providing recent advancements in designing Real-Time Imaging applications, Rice Grading System for Embedded Image Processing includes fundamental theory, soft-computing algorithms and extensive step-by-step guide to generate automatic HDL codes through Simulink®. The text includes the design of hardware compatible fixed-point codes for the development of automatic image processing based rice grading system. The topics covered are approached with the aim of reducing complexity in implementation of image processing operations on hardware platforms like FPGAs. Features flowcharts, code-snippets of fixed-point codes, VHDL source codes, images of qualitative results and tables of quantitative analysis Provides step-by-step guide to design & synthesize Simulink® models of image processing system for automatic generation of HDL followed by FPGA synthesization on Spartan6 Provides extensive literature review on embedded imaging from past 16 years. The book provides an exclusive approach towards the FPGA implementation of image processing based operations and will serve as a reference guide for development of handheld imaging devices 96 pp. Englisch.



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