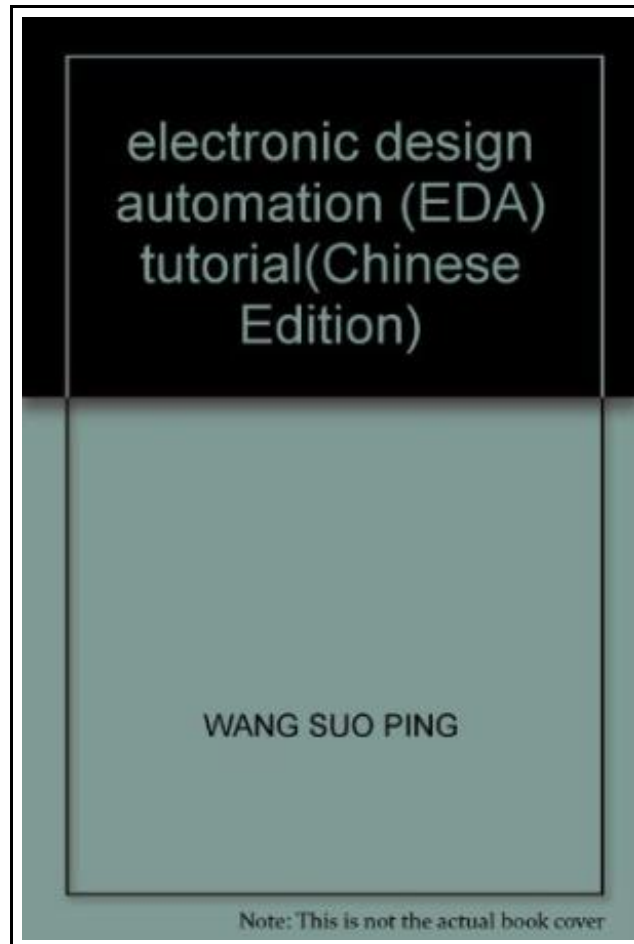


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paperback. Book Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment. Paperback. Publisher: Electronic Science and Technology Pub. Date :2000-02-01. This book introduces electronic design automation (EDA) of the basics. including hardware description language (VHDL) . programmable logic devices (PLD). can use the circuit analysis program (PSPICE). printed circuit board design (PCB) and application specific integrated circuit (ASIC) designs. Book for the EDA design entry materials. for electric class institutions of higher learning or graduate students in the undergraduate use. may also be used in electronic systems design research. management. technical officers. Contents: Introduction Chapter 1.1 of electronic design automation CAD 1.2 EDA and electronic system design tutorial Introduction Chapter 2.1 Digital System Design Digital System Design Digital System Design 2.1.1 Overview 2.1.2 Digital systems design process should be considered the main factor 2.2 2.3 digital system model detailed logic diagram 2.4 Algorithm analysis of small digital systems Figure 2.6 MDS 2.5 MDS circuit diagram of micro-level design programming exercises 2.7 References Chapter 3 Hardware Description Language (VHDL) 3.1 Overview 3.2 VHDL model structure 3.2 .1 design entity (DesignEntities) 3.2.2 structure (ArchitectureBodies) 3.3 Identifiers. data objects. data types and attributes 3.3.1 Identifier (Identifiers) 3.3.2 Data Objects (DataObjects) 3.3.3 Data Types (DataTypes) 3.3.4 Subtype (Subtypes) 3.3.5 Properties (Attributes) 3.4 VHDL description of the most basic method of 3.4.1 Process statement (ProcessStatements) 3.4.2 Parallel (concurrent) statement (ConcurrentStatements) 3.4.3 sequential statements (SquentialStatements) 3.4 .4 square off statement (AssertStatements) 3.5 Combinational Logic Design (CombinatinalLogicDesign) 3.5.1 Overview 3.5.2 basic logic gates encoder and decoder described 3.5.3 Demux 3.5.4 3.5.5 Addition comparator device description (Adders) 3.6 sequential logic design (SequentialLogicDesign) 3.7 state machine design (StateMachineDesign) 3.8 Design Example Exercises References Chapter programmable logic devices and development tools for programmable logic devices 4.1 Overview 4.2 PAL and...



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